**ASIPMEISTER -TUTORIAL**

1. Login to any ***i80labpcXX.ira.uka.de*** directly or using SSH or using X2Go Client. For example login as ***asip04*** into ***i80labpc02.ira.uka.de***
2. Open shell terminal from the start menu. It should be in your default home directory. Type “***pwd***”

asip04@i80labpc04:~:$pwd

/home/asip04

1. Create a new directory for your Lab e.g. “***ASIP\_SS17”***

asip04@i80labpc04:~:$mkdir ASIP\_SS17

asip04@i80labpc04:~:$cd ASIP\_SS17/

1. Create a new directory for your lab session in “***ASIP\_SS17”*** e.g. “***Session1”***

asip04@i80labpc04:~/ASIP\_SS17:$mkdir Session1

asip04@i80labpc04:~/ASIP\_SS17:$cd Session1/

1. Create a directory for your ASIP project in “***Session1”***

asip04@i80labpc04:~/ASIP\_SS17/Session1:$mkdir ASIPMeisterProjects

asip04@i80labpc04:~/ASIP\_SS17/Session1:$cd ASIPMeisterProjects/

1. For each ASIPmeister CPU create a separate in “***ASIPMeisterProjects”***. For example copy template project and rename it e.g. “***dlx\_basis”*** for ASIPmeister CPU “***dlx\_basis.pdb***”

asip04@i80labpc04:~/ASIP\_SS17/Session1/ASIPMeisterProjects:$cp -r /home/asip00/ASIPMeisterProjects/TEMPLATE\_PROJECT ./dlx\_basis

asip04@i80labpc04:~/ASIP\_SS17/Session1/ASIPMeisterProjects:$ls

dlx\_basis

1. Set the parameters and settings of the ASIP project in “***env\_settings***”
2. Open ASIPMeister CPU in the respective directory i.e. in dlx\_basis

asip04@i80labpc04:~/ASIP\_SS17/Session1/ASIPMeisterProjects/dlx\_basis:$ASIPmeister dlx\_basis.pdb &

1. Modify the CPU in ASIPmeister and generate the required files. A “meister” directory will be created in your ASIP project directory i.e. in “***dlx\_basis***”
2. For another CPU copy template project and rename it e.g. “***dlx\_basis\_opt”*** for ASIPmeister CPU “***dlx\_speed.pdb***”

asip04@i80labpc04:~/ASIP\_SS17/Session1/ASIPMeisterProjects:$cp -r /home/asip00/ASIPMeisterProjects/TEMPLATE\_PROJECT ./dlx\_basis\_opt

asip04@i80labpc04:~/ASIP\_SS17/Session1/ASIPMeisterProjects:$ls

dlx\_basis dlx\_basis\_opt

1. Set the parameters and settings of the ASIP project in “***env\_settings***”
2. Open ASIPMeister CPU in the respective directory i.e. in dlx\_basis\_opt

asip04@i80labpc04:~/ASIP\_SS17/Session1/ASIPMeisterProjects/dlx\_basis\_opt:$ASIPmeister dlx\_speed.pdb &

1. Modify the CPU in ASIPmeister and generate the required files. A “meister” directory will be created in your ASIP project directory i.e. in “***dlx\_basis\_opt***”